

Electronic Acknowledgement Receipt

EFS ID:	5690787
Application Number:	10072145
International Application Number:	
Confirmation Number:	1448
Title of Invention:	Semiconductor device and method of providing regions of low substrate capacitance
First Named Inventor/Applicant Name:	Guy E. Averett
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Filer Authorized By:	Kevin Brian Jackson
Attorney Docket Number:	ONS00317
Receipt Date:	13-JUL-2009
Filing Date:	07-FEB-2002
Time Stamp:	16:45:39
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no
File Listing:	

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Miscellaneous Incoming Letter	ONS00317D01_Response.pdf	166605 85146188f13c3f0dcb6a8b719275ac3172793	no	4

Warnings:

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National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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